RGB Video Ampifier for High Resolution Monitors

Description

The U2203B-A is a wideband video ampifier IC system especially designed for use in high resolution RGB-monitors. The IC includes three well matched video amplifiers, three gated differential amplifiers for black level clamping and brightness control and the function for simultaneously contrast control. Separate adjustment of the maximum gain of each channel is also possible.

Features

- 3 channels, high bandwidth (90 MHz @ –3 dB)
- Matched attenuators for contrast control
- Black level clamping for brightness control
- Independent gain control of each amplifier
- ESD protection according to MIL-STD. 883

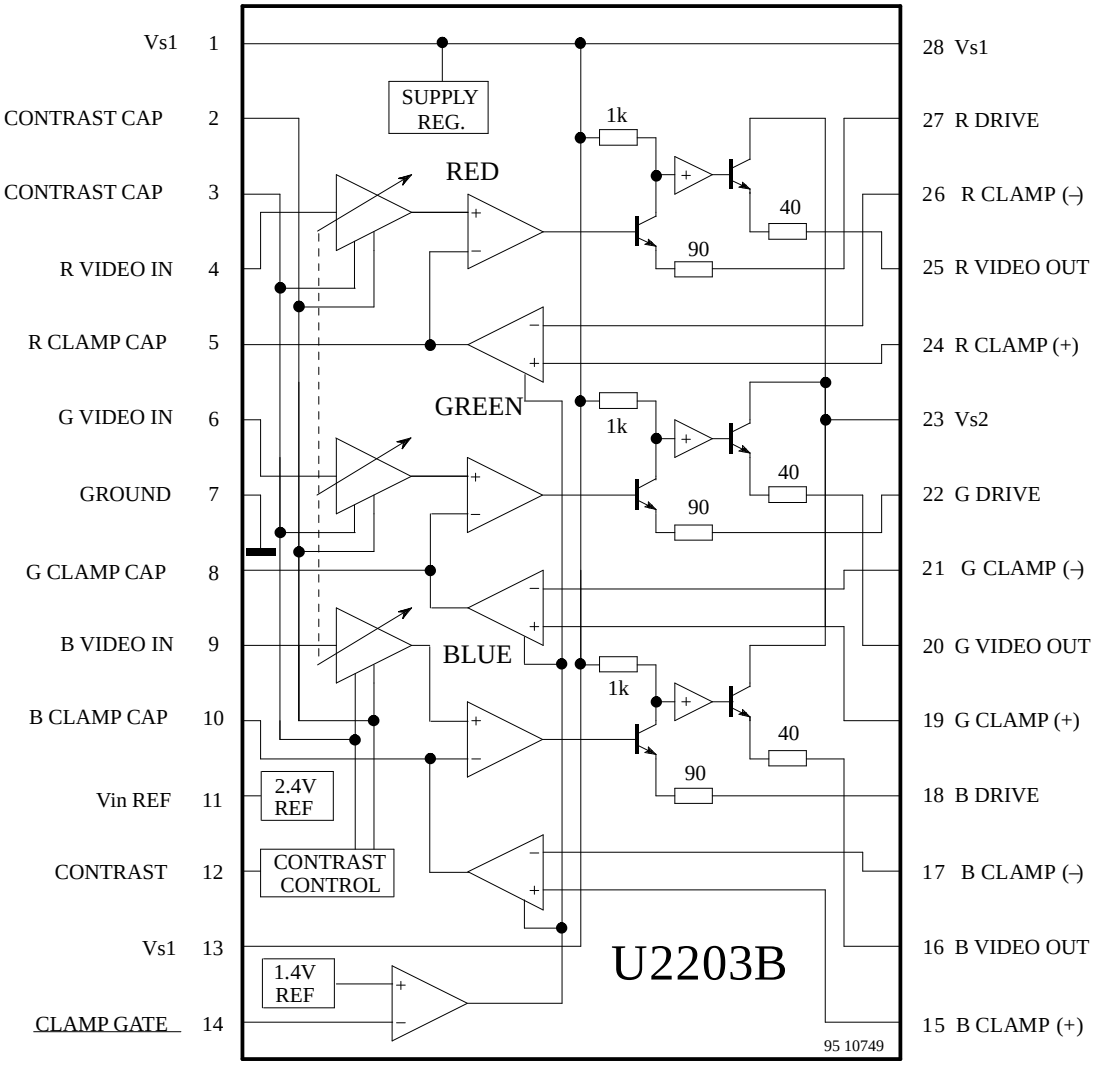


Figure 1. Block diagram

Pin Description

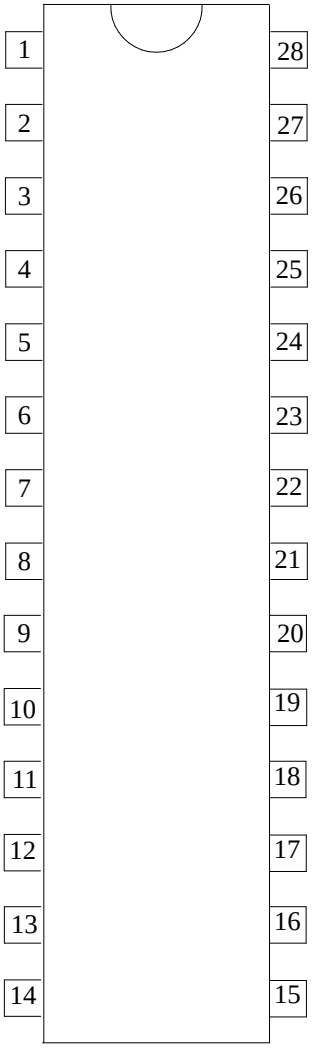


Figure 2. Pinning

Pin	Symbol	Function
1	V_{cc1}	
2		Contrast cap.
3		Contrast cap.
4		Video input (R)
5		Clamp cap. (R)
6		Video input (G)
7	GND	Ground
8		Clamp cap. (G)
9		Video input (B)
10		Clamp cap. (B)
11		Bias voltage (RGB-inputs)
12		Contrast control
13	V_{cc1}	
14		Inverted clamp gate
15		Clamp amplifier (B+)
16		Video output (B)
17		Clamp amplifier (B–)
18		Drive resistor (B)
19		Clamp amplifier (G)
20		Video output (G)
21		Clamp amplifier (G–)
22		Drive resistor (G)
23	V_{cc2}	Outputs
24		Clamp amplifier (R+)
25		Video output (R)
26		Clamp amplifier (R–)
27		Drive resistor (R)
28	V_{cc1}	

Absolute Maximum Ratings

Parameters		Symbol	Value	Unit
Supply voltage	Pins 1, 13, 28 and 23	V_S	13.5	V
Input voltage at all other pins		V_{in}	o toV_S	V
Video output current	Pins 16, 20 and 25	I_V	28	mA
Junction temperature		T_j	125	°C
Storage temperature range		T_{stg}	–40 to 125	°C

Operating Range

Parameters	Symbol	Value	Unit
Ambient temperature range	T_{amb}	0to 70	°C

Thermal Resistance

Parameters	Symbol	Value	Unit
Junction ambient	R_{thJA}	36	K/W

Electrical Characteristics

Test conditions unless otherwise specified, reference point Pin 7, T_{amb} = 25°C,
V_{S1} = V_{S2} = 12 V, V_{12} = 6 V (contrast); V_{14} = 0 V (inv. gating); V_{15} = V_{19} = V_{24} = 2 V (brightness)

Parameters	Test Conditions / Pins	Symbol	Min.	Typ.	Max.	Unit
DC-values						
Supply current	V_{S1} only, Pins 1, 13 and 28	I_{S1}		60	70	mA
Input bias voltage	Pin 11	V_{inREF}	2.3		2.5	V
Input bias current	Pins 4, 6 and 9	I_{inREF}	1	5	10	µA
Switching voltage gating “on”	Pin 14	VG “L”	0.8	1.3		V
Switching voltage gating “off”	Pin 14	VG “H”		1.5	2.0	V
Switching current gating ”on”	Pin 14 = 0.8 V Pin 14	IG “L”	–1	–0.01		µA
Switching current gating ”off”	Pin 14 = 2.0 V Pin 14	IG “H”		3	5	µA
Charge current clamping capacitor	Pin 5, 8, 10 = 0 V Pins 5, 8 and 10	ICL+	0.8	1.0	1.2	µA
Discharge current clamping capacitor	Pin 5, 8, 10 = 5 V Pins 5, 8 and 10	ICL–	–1.2	–1.0	–0.8	µA
Video outputs, V_O	**Pins 16, 20 and 25**					
Low level	V_{clamp} = 0 V	$V_{O“L”}$		0.8		V
High level	V_{clamp} = V_S	$V_{O“H”}$		8.6		V
Output offset between two amplifier	V_{15}, V_{19}, V_{24} = 2 V	ΔV_O (2 V)			±50	mV
	V_{15}, V_{19}, V_{24} = 4 V	ΔV_O (4 V)			±50	mV
Video gain						
Maximum video gain	V_{12} = 12 V	G_{Vmax}		17.7		dB
Middle video gain	V_{12} = 5 V	GV_{mid}		8.5		dB
Dynamic gain range		GVD		50		dB
Video gain match	V_{12} = 12 V V_{12} = 5 V V_{12} = 2.2 V 1)	GVM_{max} GVM_{mid} $GVM_{–30}$		±0.1 ±0.1 ±0.3		dB dB dB
Video bandwidth						
unpeaked (C = 0 pF) optimal peaking (C = 27 pF)	V_{12} = 12 V ($f_{–3dB}$) V_{12} = 12 V ($f_{–3dB}$)	BWV BWV_P		65 90		MHz MHz
Video cross talk	f_{IN} = 10 kHz f_{IN} = 10 MHz	CT_{10kHz} CT_{10MHz}	–50	 –47		dB dB

1) Measured 30 dB below maximum gain

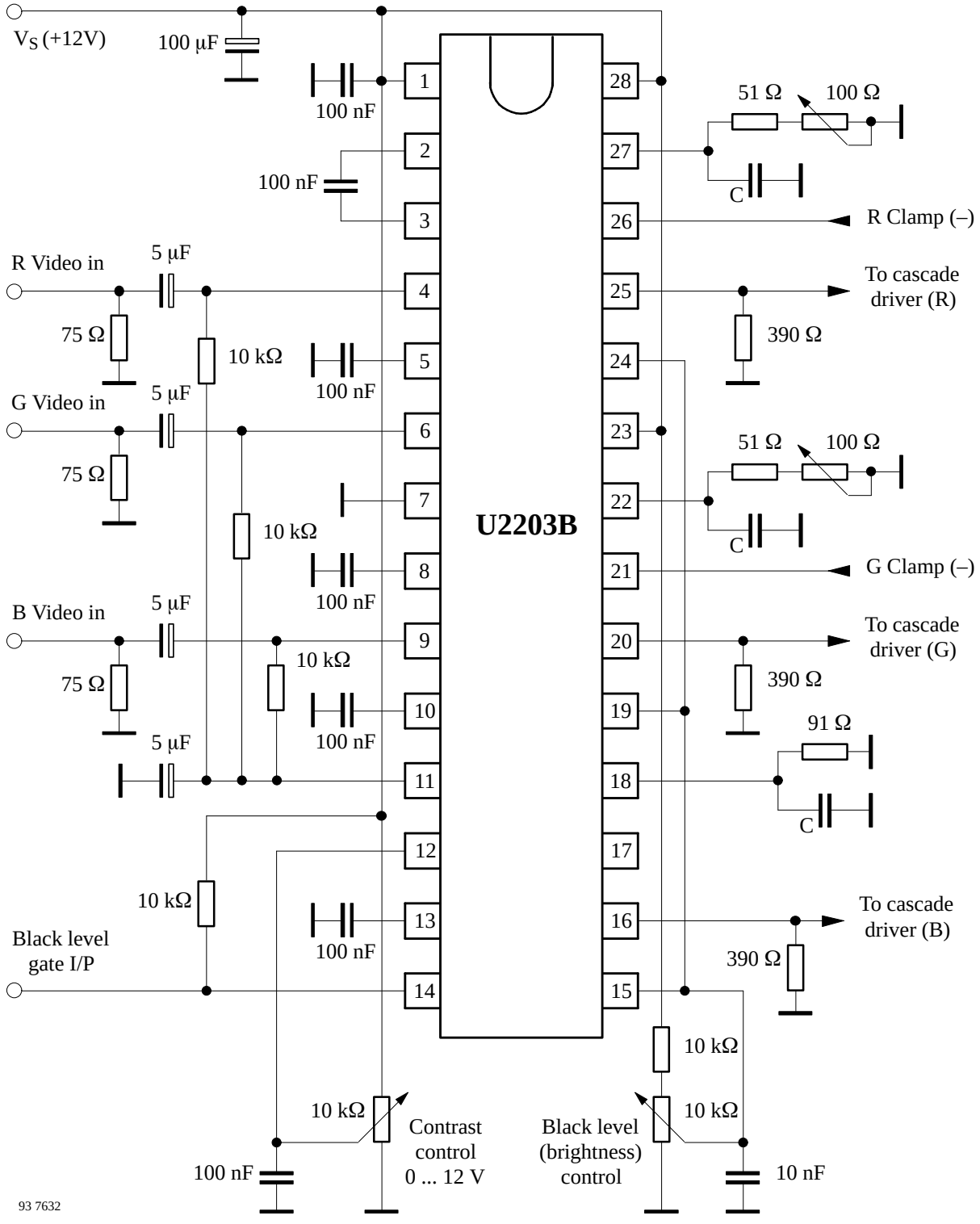
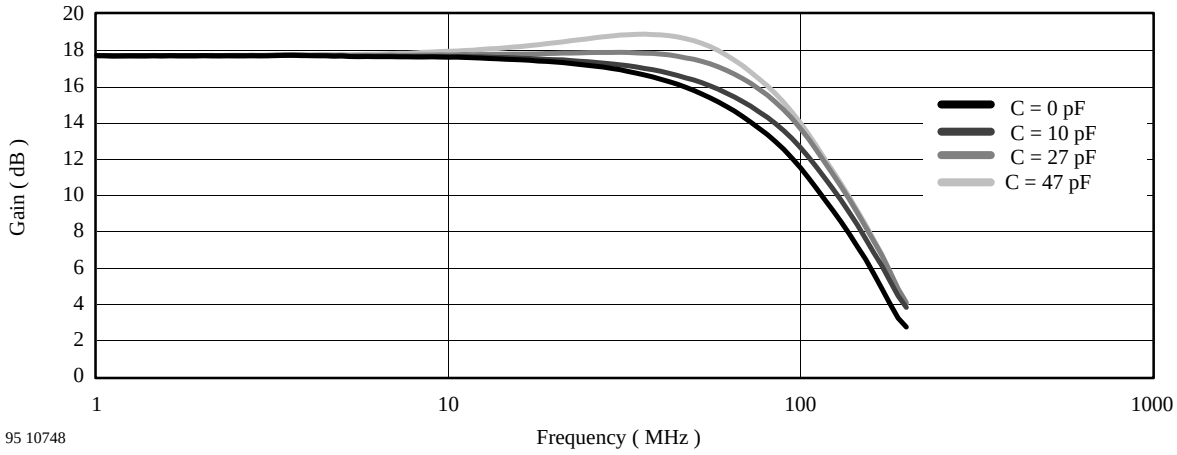
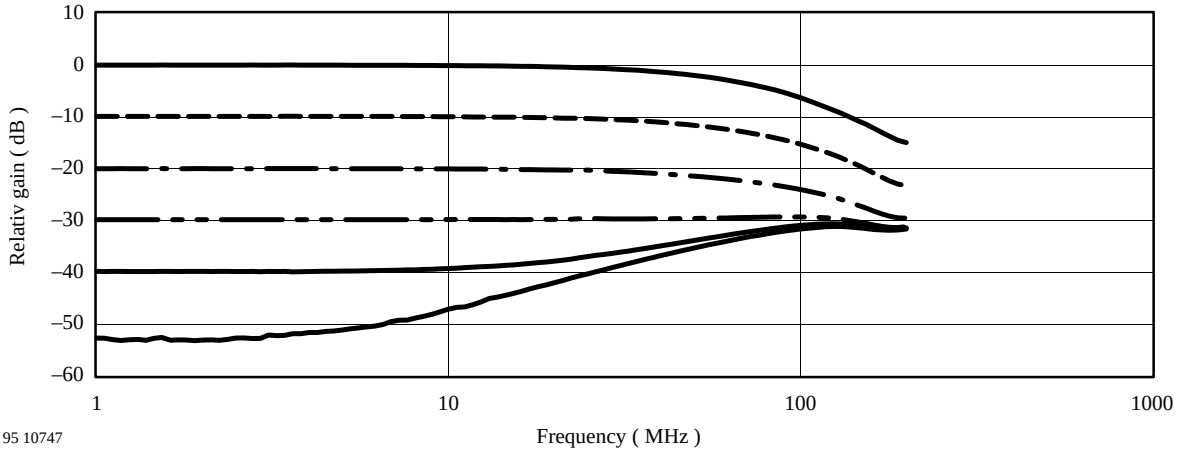


Figure 3. Typical application circuit

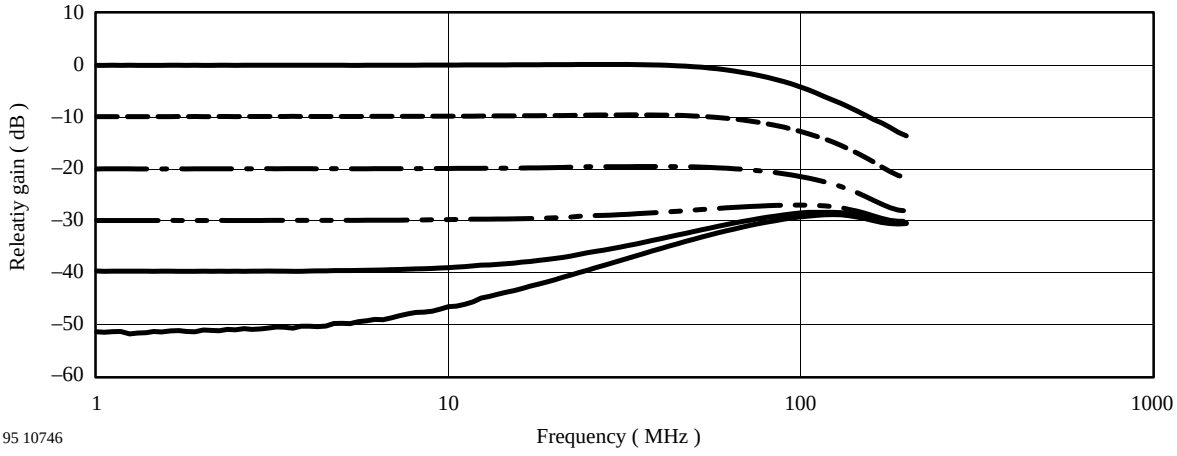
AC Characteristics



Gain vs. frequency (various peaking), V_{12} = 12 V (contrast), $V_{15} = V_{19} = V_{24}$ = 4 V (bightness)



Relativ gain vs. frequency (no peaking), V_{12} = variable (contrast), $V_{15} = V_{19} = V_{24}$ = 4 V (brightness), 0 dBrel = 17.7 dB, C = 0 pF

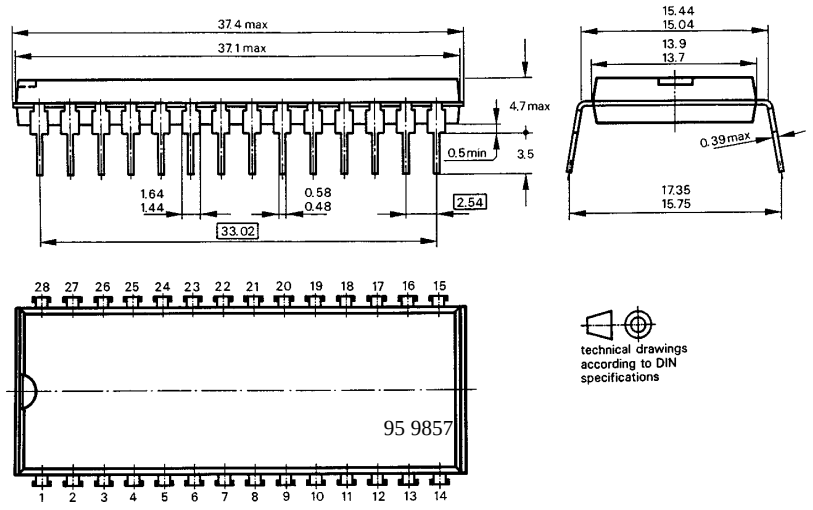


Rel. gain vs. frequency (optimized peaking), V_{12} = variable (contrast), $V_{15} = V_{19} = V_{24}$ = 4 V (brightness), 0 dBrel = 17.7 dB, C = 27 pF

U2203B

Dimensions in mm

Package: DIP28



Ozone Depleting Substances Policy Statement

It is the policy of **TEMIC TELEFUNKEN microelectronic GmbH** to

1. Meet all present and future national and international statutory requirements.

2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

TEMIC TELEFUNKEN microelectronic GmbH semiconductor division has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively

2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA

3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

TEMIC can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

TEMIC TELEFUNKEN microelectronic GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany
Telephone: 49 (0)7131 67 2831, Fax number: 49 (0)7131 67 2423